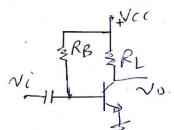
## Subjects:

- 1. Power Amplifiers (class-A, class-B, class-AB and class-C).
- 2. Differential amplifiers.
- 3. Operational amplifier applications.
- 4. Frequency response.
- 5. Oscillators.
- 6. Multivibrators (bistable, monostable and astable).
- 7. Digital circuits (logic families, combinational logic circuit, ROMs, RAMs, A/D and D/A converters).

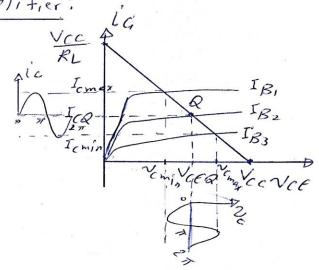
## References:

- 1. Microelectronics digital and analog circuits and systems by Jacob Millman.
- 2. Electronic circuits discrete and integrated by Schilling.

1. Class - A Power Amplifier:



Series-Fed Class-A power Amplifier



The amplifier is class-Aif its output remain in the active region during a complete cycle of a sinewave input signal. Icmax & ICR

In this case is biased at VCFR = VCC/2, which is midway between saturation and cutoff, under these circumstances the non linear distortion is negligible.

- Supplied Power (Pcc) = Vcc ICR from D.C. Source

- Power transferred to load (PL) = PLac + PLdc

Plac is the average ac power can be delivered to load - Plac=Verms × Icrms = Vermax Icmax = Temax RL

Pldc is the dc Power delivered to load

Pldc = ICR PL

That will be when I cmax = I CQ and Vernax = VEQ That = \frac{ICQRL}{2VCCICQ} = \frac{\text{ICQ}RL}{4VCEQ \text{ICQ}} \times \text{100//, = 25//,}

The disadvantage of this amplifier is the low efficiency but, the distortion is less than the other type of amplifiers.

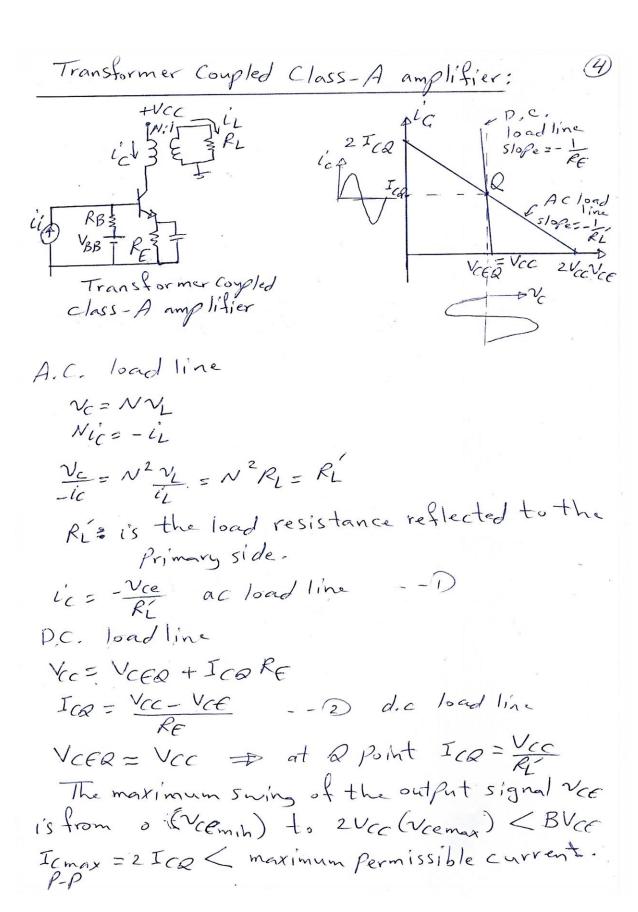
Ex: The class-A amplifier is biased at VCE=12V, R\_1=501. The output voltage maximum or penk is 8V. Find the load power, source power, the power dissipated in the Collector and the efficiency.

 $P_{Lac} = \frac{I_{Cmax} V_{Cmax}}{2} = \frac{V_{Cmax}}{2R_L} = \frac{(8)}{100} = 0.64W$   $P_{CC} = V_{CC} I_{CQ} = \frac{2V_{CEQ}^2}{R_L}$   $= \frac{2(12)^2}{5.76W} = 5.76W$ 

PLdc = VCER = (12) = 2.88~

Pc = Pcc - Plac - Plac = 5.76w - 0.64w - 2.88w = 2.24w

1= Plac 1/00/ = 0.64 1/00/ =11-11/6



1 = 50/, Icmax = 1 (Q Exi Transformer coupled Class-A amplified

Pomax = 4w, BVCED = 40v, icmax = 1A, with

transformer coupling to the lon load, redesign

the amplifier for maximum power transfer to

the load, specify the required supply voltage

the power dissipated in the load, and the transf
ormer turns ratio: N.

Solution: from the quiescent point which will provide max. Power transfer to the load can be obtained.

$$ICQ = \sqrt{\frac{Rc_{max}}{N^2 R_L}} = \sqrt{\frac{0.4}{N^2}} = \frac{0.63}{N} A$$

$$VCEQ = \sqrt{\frac{Pc_{max}N^2 R_L}{N^2}} = 6.3N V$$